AI reply for query: Compare the SerDesDesign.com and Siemens HyperLynx channel simulators.

Date: 11/6/2026 9:50 AM PST

Google.com AI Overview

Comparing **SerDesDesign.com** and **Siemens HyperLynx** channel simulators highlights a choice between a specialized, lightweight cloud/web-based behavioral modeling tool and a comprehensive, enterprise-level PCB signal and power integrity EDA suite. [1, 2]

Core Overview

- **SerDesDesign.com:** A dedicated, web- and cloud-based behavioral modeling and simulation environment (created by John Baprawski) focused on multi-gigabit serial link architectures, IBIS-AMI model creation, equalization tuning, and rapid channel/crosstalk evaluation. [1, 2, 3]
- **Siemens HyperLynx:** An industry-standard, deeply integrated commercial EDA platform within Siemens' portfolio for full PCB-level verification, including pre-layout topology exploration, 2D/3D field solving, power integrity (DC/AC), and automated post-route compliance checking. [1, 2, 3]

Key Comparison Dimensions

Feature / Capability	SerDesDesign.com Channel Simulator	Siemens HyperLynx Channel Simulator
Primary Scope	Focused SerDes/repeater system behavioral simulation and IBIS-AMI IP generation.	Full-board PCB signal integrity (SI), power integrity (PI), EMI, and multi-gigabit verification.
Layout Integration	Standalone / web-driven; uses S-parameter files (.s4p) and impulse responses rather than direct layout extraction.	Deeply embedded with PCB layout/schematic flows (LineSim, BoardSim, and automated 3D via/trace extraction).

Signaling Support	NRZ and PAM-4.	NRZ and PAM-4 with over 400 built-in protocol standards (PCIe, USB, Ethernet, MIPI, HDMI, etc.).
Equalization & Models	Parameterized behavioral blocks (FFE, CTLE, DFE, AGC, CDR) and custom IBIS-AMI generator/parser.	Comprehensive IBIS-AMI batch wizards, vendor-specific model support, and automated equalization sweeps.
Field Solver & 3D	Relies on external S-parameter models or impulse files (no native full-wave board field solver).	Built-in fast 2D field solver + automated intelligent 3D EM extraction for vias and critical discontinuities.
Simulation Modes	Statistical and bit-by-bit simulation, BER extrapolation, and eye-density/bathtub generation.	Statistical and bit-by-bit analysis, batch wizards, multi-net post-route verification, and comprehensive HTML pass/fail reports.
Cost & Access	Offers free/low-cost cloud-based and downloadable single-user utility options.	Commercial enterprise licensing via Siemens EDA.

When to Use Which?

- **Choose SerDesDesign.com if:** You are an IC/SerDes IP architect, signal integrity engineer, or consultant needing a quick, portable way to build or test custom IBIS-AMI models, evaluate TX/RX equalization trade-offs, run BER extrapolation, or experiment with serial/repeater links without tying up a heavy enterprise EDA license. [1, 2, 3]
- **Choose Siemens HyperLynx if:** You are a board-level hardware or PCB designer who needs to extract actual traces, connectors, and vias straight from a physical layout, simulate power-delivery networks (PDN), check hundreds of post-route nets against formal protocol compliance wizards, and run full-wave 3D electromagnetic solvers. [1, 2, 3]