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For the past 10+ years, John Baprawski has provided cost-efficient high-quality IBIS-AMI models to 40+ high speed digital (HSD) integrated circuit (IC) companies using his IBIS-AMI Model Development Environment for use in any standards compliant SerDes system channel simulator. That work has relied on his free web-based tools including his SerDes Channel Simulator (<a href="https://www.serdesdesign.com/home/">https://www.serdesdesign.com/home/</a>). That work has also focused on collecting correct circuit data and automating the process for converting that data into IBIS-AMI models.

This paper highlights the process for creating a perfect Rx AFE with Nonlinearity IBIS-AMI model from circuit data.

- Small signal levels: Perfect = IBIS-AMI model exactly agrees with circuit.
- Large signal levels: Perfect = IBIS-AMI model agrees with circuit for peak and step levels with some variation in the pulse shape allowed as the signal goes into compression.

### **Overview**

Be sure to read these two documents first:

READ\_ME\_FIRST\_-\_License\_Agreement.pdf

READ ME SECOND - Instructions.pdf

This Rx Tool has this directory structure:

- C:/AMI/AFE/ModelBuilder SerDesDesign.com IBIS-AMI Build Environment (partial).
- C:/AMI/AFE/README Documents; including this one.
- C:/AMI/AFE/RxAFE NL Modeling Tool Rx AFE with Nonlinearity Modeling Tool.
- C:/AMI/AFE/RxCTLE\_SS\_Modeling\_Tool Rx CTLE Small Signal Modeling Tool.
- C:/AMI/AFE/RxNonlinearityData directory for the circuit nonlinearity data files.
- C:/AMI/AFE/RxWaveformLargeSignalData directory for the circuit data files.
- C:/AMI/AFE/RxWaveformSmallSignalData directory for the circuit data files.
- C:/AMI/AFE/RxAMI\_Solution\_SerDes\_AFE\_NL SerDesDesign.com IBIS-AMI build directory.

# **Circuit Data Collection**

The circuit is assumed to be for a SerDes receiver (Rx) nonlinear analog front end (AFE) with differential inputs that interface with a differential SerDes channel.

The data to be collected is:

- The Rx circuit input differential impedance versus frequency.
- The Rx circuit input waveform.
- The Rx circuit AFE output waveform for each AFE state and input drive level.

For detail instruction on this required circuit data see these reports:

#### Modeling an NLTV RxSerDes AFE with Nonlinearity wih CircuitData.pdf

Place this circuit data into the directories:

- C:\AMI\AFE\RxNonlinearityData.
- C:\AMI\AFE\RxWaveformLargeSignalData.
- C:\AMI\AFE\RxWaveformSmallSignalData.

Within these directories create the file InputFileNameList.txt (LS\_InputFileNameList.txt for the large signal directory) which is to contain a list of all the files in the desired order.

### Setting up the Small Signal RxCTLE\_SS Modeling Tool

It is assumed that when the AFE is running in its small signal region, then it is essentially being used as a continuous time linear equalizer (CTLE). In this section, the term CTLE is used to mean operating the AFE in this small signal region.

The RxCTLE SS NL Modeling tool is in the directory C:\AMI\AFE\RxCTLE SS Modeling Tool.

Within that directory, the file DataFiles\RxCTLE\_SS\_Modeling.txt needs to be set up by the user.

This file contains two lines. In the following, the user entered values are within angle braces <...>.

**Line 1:** SetupAnalysis BitRate <bit\_rate> SamplesPerBit <samples\_per\_bit> NumStepResp <num\_step\_resp>

<br/><br/>t\_rate>: this is the maximum bit rate for an NRZ data pattern.

<samples\_per\_bit>: can remain at 32.

<num\_step\_resp>: number of CTLE states.

**Line 2:** RxCTLE\_Modeling DirName <small\_signal\_dir\_name> IBIS\_SParamFile <s2p\_filename> SkipLines <skip\_lines> ExtractionTStart <extraction\_tstart> ExtractionTLength <extraction\_tlength>

<small\_signal\_dir\_name>: can remain at C:\AMI\AFE\RxWaveformSmallSignalData.

<s2p\_filename>: Name of the s2p data file.

<skip\_lines>: Number of lines at the top of the waveform files to skip.

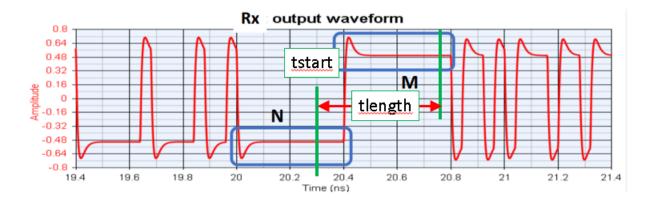
<extraction\_tstart>: the time stamp in the waveform file used to start waveform extraction.

<extraction\_tlength>: the time length after the tstart for the end of the waveform extraction.

#### Discussion:

It is assumed that all CTLE waveform files use the same NRZ data pattern and the same time samples. As such, all files have the same NRZ segment with N zeros and M ones. Set tstart to a reference time before the zero-to-one transition. Set tlength to the point after this transition and before the next transition.

This figure shows this concept.



# **Running the RxCTLE Modeling Tool**

The RxCTLE Modeling tool is run by selecting the batch file C:\AMI\AFE\RxCTLE SS Modeling Tool\RxCTLE SS Modeling.bat

When this is done, a Windows Command window pops up and displays the running simulation status. The following screen captures show the starting and ending screen captures.

```
C:\AMI_CTLE\RxCTLE_SS_Modeling_Tool>RxCTLE_SS_Modeling_Tool.exe

*** Starting RxCTLE_Modeling_Tool
BitRate = 2.57812e+10
SamplesPerBit = 32
NumStepResp = 32
*** Completed Analysis Setup.
DirName = C:\AMI_CTLE\RxWaveformSmallSignalData
IBIS_SParamFile = RxSerDes_CTLE.s2p
SkipLines = 4
ExtractionTStart = 5.05e-08
ExtractionTLength = 1.4e-09
*** Completed RxCTLE_Modeling_Tool Setup.
```

```
Exiting RxCTLE_Modeling_Tool with success.

Hit any key to continue and exit this program.
```

The process produces its log files in the RxWaveformSmallSignalData directory: Log\_RxCTLE\_Modeling\_Tool.log.

This process takes several steps along the way. Each step produces a log file and other output files into the RxWaveformSmallSignalData directory.

Step 1: Combine the circuit waveform files.

```
*** Starting RxCTLE_ResampleWaveform
Running RxCTLE_ResampleWaveform Sweep
Running RxCTLE_ResampleWaveform CreateOutFile
For detail log, see file: C:\AMI_Test\WaveformSmallSignalData\Log_ResampleWaveformData.log
Generated file: C:\AMI_Test\WaveformSmallSignalData\Combined.csv
*** Completed RxCTLE_ResampleWaveform
```

Step 2: Generate the IBIS S4P file and IBIS impulse response.

Step 3: Extract the desired waveform segment from the combined waveform file.

Step 4: Deembed the input waveform segment with IBIS impulse applied from the output waveform segment to achieve the step response data for the AMI model.

```
*** Starting RxCTLE_DeembedInputResp
     Running Get OutputStepData.
     Running Write Final and Peak Output Step Values.
    Running Get InputStepData.
     Running Write Input Step Gain Levels.
    Running Get IBIS_ImpulseData.
    Running Derive Output Gain and Peaking Ratios; this may take several minutes.
    Running Convolve Input Step with IBIS Impulse Running Deembed Input with IBIS from Output.
    Running Write Input Step with IBIS.
Running Validate Deembedded Output Steps
    For detail log, see file: C:\AMI_Test\WaveformSmallSignalData\Log_DeembedInputResp.log
    Generated files:
                 {\tt C:\AMI\_Test\WaveformSmallSignalData\Combined.InputStepExtracted.InputStepLevels.csv}
                 C:\AMI_Test\WaveformSmallSignalData\Combined.OutputStepExtracted.InputDeembeded.GainAndPeakingValues.csvC:\AMI_Test\WaveformSmallSignalData\Combined.OutputStepExtracted.InputDeembedded.csv
                 {\tt C:\backslash AMI\_Test\backslash WaveformSmallSignalData\backslash Combined.OutputStepExtracted.InputWithIBISDeembedded.csv}
                 {\tt C: \AMI\_Test \WaveformSmallSignalData \Combined.InputStepExtracted.WithIBIS\_Impulse.csv} \\
*** Completed RxCTLE_DeembedInputResp
```

The log file, Log\_DeembedInputResp.log, shows the internal test results comparing the resultant IBIS-AMI model responses with the original data output responses.

```
*** Begin validation process by convolving deembedded step responses with input with IBIS impulse responses and comparing to the original step responses.

*** During validation the maximum final step value % error = 0.000483397 %.

*** During validation the maximum peak step value % error = 0.3205 %.

*** Completed validation process.
```

This verification step shows excellent between model and data agreement.

One file contains all the combined waveform data: Combined.csv

Two files are used in creating the Rx AFE NL IBIS-AMI model:

- <your file name>.s2p.s4p.
- Combined.OutputStepExtracted.InputWithIBISDeembedded.csv

# Setting up the RxAFE\_NL Modeling Tool

The RxAFE\_NL Modeling tool is in the directory C:\AMI\AFE\RxAFE\_NL\_Modeling\_Tool.

Within that directory, the file DataFiles\RxAFE\_LS\_Modeling.txt needs to be set up by the user.

This file contains three lines. In the following, the user entered values are within angle braces <...>.

**Line 1:** SetupAnalysis BitRate <bit\_rate> SamplesPerBit <samples\_per\_bit> Num\_SS\_StepResp <num\_step\_resp> Num\_LS\_Resp <num\_ls\_resp> Num\_LS\_Levels <num\_ls\_levels>

<br/><bit rate>: this is the maximum bit rate for an NRZ data pattern.

<samples\_per\_bit>: can remain at 32.

<num step resp>: number of small signal AFE states.

<num\_ls\_resp>: number of large signal AFE states. Can be either the same as
<num\_step\_resp> or 3 where 3 implies that the three states are for minimum emphasis,
intermediate emphasis and maximum emphasis. If <num\_ls\_resp> = 3, then the large signal
model for all <num\_step\_resp> states will be derived from these 3 states.

<num\_ls\_levels>: number of input signal levels used to characterize the <num\_ls\_resp> from
small signal level to larger levels towards the AFE compression limit.

**Line 2:** RxAFE\_LS\_Modeling SS\_DirName <small\_signal\_dir\_name> LS\_DirName <large\_signal\_dir\_name> IBIS\_SParamFile <s2p\_filename> SkipLines <skip\_lines> ExtractionTStart <extraction\_tstart> ExtractionTLength <extraction\_tlength>

<small\_signal\_dir\_name>: can remain at C:\AMI\AFE\RxWaveformSmallSignalData.

Note: The small signal directory only contains the AFE small signal data.

<large\_signal\_dir\_name>: can remain at C:\AMI\AFE\RxWaveformLargeSignalData.

Note: The large signal directory include the small signal data for the <num\_ls\_resp> states.

<s2p filename>: Name of the s2p data file.

<skip\_lines>: Number of lines at the top of the waveform files to skip.

<extraction\_tstart>: the time stamp in the waveform file use to start waveform extraction.

<extraction\_tlength>: the time length after the tstart for the end of the waveform extraction.

**Line 3:** RxAFE\_NonlinearityModeling NumResamplesPerVolt <num\_samp\_pervolt> NonlinearityDirName <nonlin\_dir\_name> SkipLines <skip\_lines>

<num\_samp\_pervolt>: can remain at 100

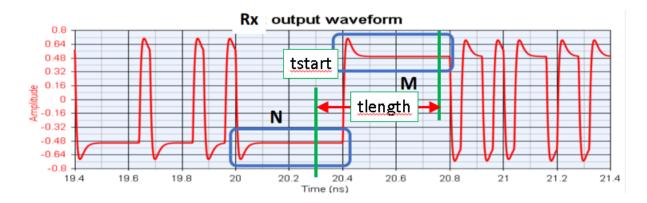
<nonin dir name>: can remain at C:\AMI\AFE\RxNonlinearityData.

<skip lines>: Number of lines at the top of the nonlinearity files to skip.

#### Discussion:

It is assumed that all AFE waveform files use the same NRZ data pattern and the same time samples. As such, all files have the same NRZ segment with N zeros and M ones. Set tstart to a reference time before the zero-to-one transition. Set tlength to the point after this transition and before the next transition.

This figure shows this concept.



It is assumed that all AFE nonlinearity files contain the DC into DC out (Vin to Vout) characteristic of the AFE with the +/- DC value large enough to drive the output to saturation.

This figure show this concept:



# Running the RxAFE\_NL Modeling Tool

The RxAFE\_NL Modeling tool is run by selecting the batch file C:\AMI\AFE\RxAFE\_NL\_Modeling\_Tool\RxAFE\_NL\_Modeling.bat

When this is done, a Windows Command window pops up and displays the running simulation status. The following screen captures show the starting and ending screen captures.

```
C:\AMI\AFE\RxAFE_NL_Modeling_Tool>RxAFE_LS_Modeling_Tool.exe    RxAFE_LS_Modeling.txt
*** Starting RxAFE_LS_Modeling_Tool
Physical address = F0-9E-4A-93-46-AD
Valid physical address.
BitRate = 2.57812e+10
SamplesPerBit = 32
Num_SS_StepResp = 32
Num_LS_Resp = 3
Num_LS_Levels = 5
*** Completed RxAFE LS Analysis Setup.
SS_DirName = C:\AMI\AFE\RxWaveformSmallSignalData
LS_DirName = C:\AMI\AFE\RxWaveformLargeSignalData
IBIS_SParamFile = RxSerDes_CTLE.s2p
SkipLines = 4
ExtractionTStart = 5.05e-08
ExtractionTLength = 1.4e-09
*** Completed RxAFE LS Data Setup.
NumResamplesPerVolt = 100
NonlinearityDirName = C:\AMI\AFE\RxNonlinearityData
SkipLines = 4
*** Completed RxAFE Nonlinearity Setup.
```

```
Exiting RxAFE_LS_Modeling_Tool with success.

Hit any key to continue and exit this program.
```

The process produces its log files in the RxWaveformLArgeSignalData directory: Log\_RxAFE\_NL\_Modeling\_Tool.log.

This process takes several steps along the way. Each step produces a log file and other output files into the RxWaveformLargeSignalData directory.

Step 1: Combine the circuit waveform files.

```
*** Starting RxAFE LS ResampleWaveform

For detail log, see file: C:\AMI\AFE\RxWaveformLargeSignalData\Log_ResampleWaveformData.log
Generated file: C:\AMI\AFE\RxWaveformLargeSignalData\Combined.csv

*** Completed RxAFE LS ResampleWaveform
```

Step 2: Generate the IBIS S4P file and IBIS impulse response.

Step 3: Extract the desired waveform segment from the combined waveform file.

#### \*\*\* Starting RxAFE LS ExtractStepResponseData

```
Generated files:
C:\AMI\AFE\RxWaveformLargeSignalData\Combined.OutputStepExtracted.csv
C:\AMI\AFE\RxWaveformLargeSignalData\Combined.InputStepExtracted.csv
*** Completed RxAFE LS ExtractStepResponseData
```

Step 4: Deembed the input waveform segment with IBIS impulse applied from the output waveform segment to achieve the data for the AMI model.

#### \*\*\* Starting RxAFE LS DeembedInputResp

```
Generated files:
C:\AMT\AFE\RxWaveformLargeSignalData\Combined.InputStepExtracted.InputStepLevels.csv
C:\AMI\AFE\RxWaveformLargeSignalData\Combined.OutputStepExtracted.InputDeembeded.GainAndPeakingValues.csv
C:\AMI\AFE\RxWaveformLargeSignalData\Combined.OutputStepExtracted.InputDeembedded.csv
C:\AMI\AFE\RxWaveformLargeSignalData\Combined.OutputStepExtracted.InputWithIBISDeembedded.csv
C:\AMI\AFE\RxWaveformLargeSignalData\Combined.InputStepExtracted.WithIBIS_Impulse.csv
*** Completed RxAFE LS DeembedInputResp
```

The log file, Log\_DeembedInputResp.log, shows the internal test results comparing the resultant IBIS-AMI model responses with the original data output responses.

```
*** Begin validation process by convolving deembedded step responses with input with IBIS impulse responses and comparing to the original step responses.

*** During validation the maximum final step value % error = 0.000780283 %.

*** During validation the maximum peak step value % error = 0.989662 %.

*** Completed validation process.

Completed Validate LS Deembedded Output Steps
```

This verification step shows excellent agreement.

Step 5: Combine the circuit nonlinearity files.

#### \*\*\* Starting RxAFE ResampleNonlinearityData

```
Generated file:
C:\AMI\AFE\RxWaveformLargeSignalData\Nonlinearity_LS_Combined.csv
*** Completed Nonlinearity_LS_Combined.csv Generation
```

Step 6: Derive the nonlinearity compensation.

```
*** Starting RxAFE Derive NL Compensation
```

#### \*\*\* Completed RxAFE Derive NL Compensation

If the <num\_ls\_resp> = 3, then the full set of compensation factors for all <num\_step\_resp> are derived from the compensation factors for these three by applying the next step.

Step 7: Extend compensation factors.

#### \*\*\* Starting RxAFE Extend LS Compensation Factors

### \*\*\* Completed RxAFE Extend LS Compensation Factors

One file contains all the combined large signal waveform data: Combined.csv

Five files are used in creating the Rx AFE NL IBIS-AMI model:

- Combined.InputStepExtracted.WithIBIS\_Impulse.InputStepLevels.csv
- Combined.OutputStepExtracted.InputWithIBISDeembedded.GainFactors.csv
- Combined.OutputStepExtracted.InputWithIBISDeembedded.GainSplitFactors.csv
- Combined.OutputStepExtracted.InputWithIBISDeembedded.RootValues.csv
- Nonlinearity\_Combined.csv

### Generating the RxAFE NL IBIS-AMI Model

This section provides instructions on building the IBIS-AMI models – only with the instructions needed to be successful. This section does not include detail discussion of these steps, the reasons for these steps, the structure of the files generated, or other such detail. That detail is available in the separate SerDesDesign.com product called the 'SerDesDesign.com IBIS-AMI Model Development Environment'. The content included with this RxAFE\_NL tool is a subset of that full model development environment.

The full IBIS-AMI Model Development Environment for Windows/Linux is available at the SerDesDesign.com store ( <a href="https://www.serdesdesign.com/home/store/products/products">https://www.serdesdesign.com/home/store/products/products</a>).

Creating IBIS-AMI models on a Windows 64-bit PC requires installing the free Microsoft Visual Studio 2019 tool.

See this link for instruction on installing Visual Studio 2019: http://www.serdesdesign.com/home/web\_documents/models/Installing\_Visual\_Studio\_2019.pdf

The RxAMI\_Solution\_SerDes\_AFE\_NL directory is used for building the IBIS-AMI model. It has this structure:



RxSerDes AFE NL is the AFE NL Rx IBIS-AMI model.

RxSerDes\_AFE\_NL\_TestWaveform is a Tx IBIS-AMI model that contains the combined set of large signal waveform data and is used to test the RxSerDes\_AFE\_NL model in a channel simulator to test Model vs Data waveforms.

Within RxSerDes\_AFE\_NL, there is a batch file: \_get\_files.bat.

You may need to change one line in this batch file. Change this line as needed:

 $copy /Y C:\AMI\AFE\RxWaveformSmallSignalData\<your\_s2p\_filename>.s2p.s4p .\RxSerDes\_AFE.s2p.s4p$ 

where you use that actual S2P file name in place of <your\_s2p\_filename>.

Running this batch file copies the required files, as identified earlier, from the RxWaveformSmallSignalData and RxWaveformLargeSgnalData directories.

With Visual Studio 2019 installed and operable, build this AMI\_Solution by running the batch file Configure-for-win64-vs2019.bat. When this batch file is run, a Windows Command window opens and runs the process to build the Visual Studio solution.

A screen captures for a successful running of this build process is shown here.

```
C:\AMI\AFE\RxAMI_Solution_SerDes_AFE_ML>mkdir build-win64-vs2019

C:\AMI\AFE\RxAMI_Solution_SerDes_AFE_ML>bkdir build-win64-vs2019

C:\AMI\AFE\RxAMI_Solution_SerDes_AFE_ML>build-win64-vs2019

C:\AMI\AFE\RxAMI_Solution_SerDes_AFE_ML\build-win64-vs2019

C:\AMI\AFE\RxAMI_Solution_SerDes_AFE_ML\build-win64-vs2019>cmake -Wno-dev -DCMAKE_INSTALL_PREFIX=../output-vs2019 -G "Visual Studio 16" -A x64 ../source -Forcing /MD.

-- Selecting Windows SDK version 10.0.22000.0 to target Windows 10.0.22621.

-- The C compiler identification is MSVC 19.29.30147.0

-- Detecting C compiler aBI info - done

-- Detecting C compiler aBI info - done

-- Detecting C compiler (compiler c:(Program Files (x86)/Microsoft Visual Studio/2019/Community/VC/Tools/MSVC/14.29.30133/bin/Hostx64/x64/cl.exe - skipped

-- Detecting C x compiler aBI info - done

-- Detecting C x compiler aBI info - done

-- Detecting C x compiler aBI info - done

-- Detecting C x compiler aBI info - done

-- Detecting C x x compiler aBI info - done

-- Detecting C x x compiler aBI info - done

-- Detecting C x x compiler aBI info - done

-- Detecting C x x compiler aBI info - done

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-- Detecting C x x compiler aBI info - done

-- Detecting C x x compiler aBI info - done

-- Detecting C x x compiler aBI info - done

-- Check for working C x x compiler aBI info - done

-- Check for working C x x compiler aBI info - done

-- Check for working C x x compiler aBI info - done

-- Check for working C x x compiler aBI info - done

-- Check for working C x x compiler aBI info - done

-- Check for working C x x compiler aBI info - done

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-- Check for working C x x compiler aBI info - done

-- Check for working C x x compiler aBI info - done

-- Check for working C x x compiler aBI info - done

-- Check for working C x x compiler aBI info - done

-- Check for working C x compiler aBI info - done

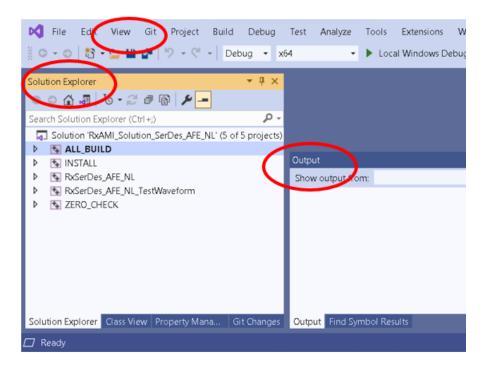
-- Check for working C x x compiler
```

Press any key to continue. You will see the directory build-win64-vs2019 within RxAMI Solution SerDes AFE NL.

Though many files were generated, only one file is used for our purpose.

See the file RxAMI\_Solution\_SerDes\_AFE\_NL.sln within the build-win64-vs2019 directory.

Double click on this \*.sln file and Visual Studio 2019 will open with this view:

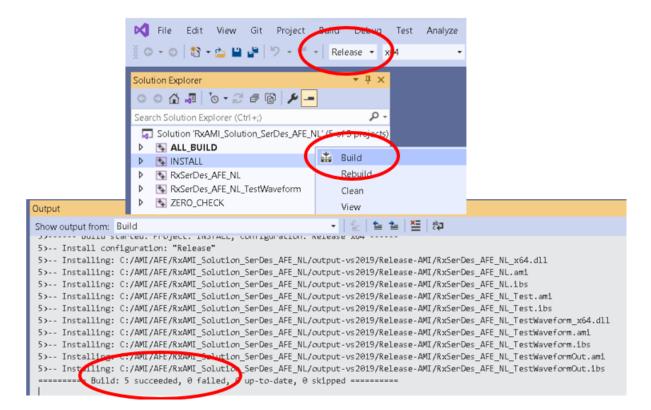


Observe the display for the 'Solution Explorer' and 'Output' windows.

You may have to use the 'View' menu on the top toolbar to display these windows.

The IBIS/AMI/Source code is in the RxSerDes\_AFE\_NL sub folder.

To build the IBIS-AMI model, Set the Solution Configuration to 'Release'. Select INSTALL, right mouse click; select 'Build'.



The Output displays build success message. The IBIS-AMI files are placed into 'output-vs2019\Release-AMI'. We are DONE!!!

Files RxSerDes\_AFE\_NL.ibs/.ami/\_x64.dll along with the s4p file can be used in any Channel Simulator on your Windows PC.

Files RxSerDes\_AFE\_NL\_Test.\* and RxSerDes\_AFE\_NL\_TestWaveform\*.\* the s4p file can be used in any Channel Simulator on your Windows PC and are used ONLY for validating the AFE\_NL waveforms (Model) with the TestWaveforms (Data).

Use of these IBIS-AMI files a channel simulator is discussed in the separate document RxSerDes\_AFE\_NL\_Use\_In\_SerDesDesign\_on\_Windows.pdf.

An unlimited number of IBIS-AMI models can be generated with this Tool and used with any Channel Simulator on this Windows PC.

# How to Remove IBIS-AMI Model Licensing Restrictions

The IBIS-AMI models generated by this Tool have the same time-based node-locked licensing restrictions as the Tool.

Use of the IBIS-AMI model on any Windows or Linux machine requires that the built-in licensing restrictions must be removed. To do this, zip up and send your

RxAMI\_Solution\_SerDes\_AFE\_NL project, with any additional instructions, to admin@serdesdesign.com.

An IBIS-AMI model with licensing restrictions removed will be sent to you after your payment in the SerDesDesign.com store ( <a href="https://www.serdesdesign.com/home/store">https://www.serdesdesign.com/home/store</a>) for an amount that is 50% of standard model pricing as defined in the response you receive from <a href="mailto:admin@serdesdesign.com">admin@serdesdesign.com</a>.

• For example: Standard pricing for an Rx IBIS-AMI model on Windows or Linux is \$2,000. So, the 50% pricing results in a price of \$1,000.

Instead of purchasing the single distributable IBIS-AMI model, you can also obtain the source code and Window/Linux build projects for your IBIS-AMI model for unlimited modification and distribution for any other IBIS-AMI models with the same architecture by purchasing IBIS-AMI Training for the IBIS-AMI model. The training fee is as published on the Store web page.

 For example: The store published price (subject to change) is 6,000 USD for IBIS-AMI Model Training (reduced for prior customers). Delivery is with source code and Windows/Linux build projects.

### **Example Tool Process with the Included Circuit Data Files**

The example circuit data files are in the RxWaveformSmallSignalData, RxWaveformLargeSignalData, and RxNonlinearityData directory.

Within the RxWaveformSmallSignalData directory are these files:

- RxSerDes\_CTLE.s2p the Rx circuit differential input S-parameters.
- CTLE\_<n>.tim the 32 waveform files (n=1 to 32) for the 32 CTLE states. These files were generated using the Keysight ADS Spice circuit simulator and use the ADS text file
   \*.tim format. The top four lines contain header information and are to be skipped.
  - Any circuit simulator text output file can be used provided it follows the required format.
- InputFileNameList.txt the text file with 32 lines listing the 32 CTLE waveform file names.

Within the RxWaveformLargeSignalData directory are these files:

- RxSerDes\_CTLE.s2p the Rx circuit differential input S-parameters (this must be the same as in the RxWaveformSmallSignalData directory).
- CTLE\_1\_50m, ..., CTLE\_1\_800m, CTLE\_8\_50m, ..., CTLE\_8\_800m, CTLE\_31\_50m, ..., CTLE\_31\_800m.tim. These are files with the least emphasis (CTLE\_1), intermediate emphasis (CTLE\_8) and maximum emphasis (CTLE\_31) each for 5 peak-to-peak input

levels (50 mv, 200mv, 400mv, 600mv, 800mv). This is a total of 15 files. These files were generated using the Keysight ADS Spice circuit simulator and use the ADS text file \*.tim format. The top four lines contain header information and are to be skipped.

- Any circuit simulator text output file can be used provided it follows the required format.
- Note: Instead of large signal data for just 3 CTLE states (15 files), it would be better to have large signal data for all 32 CTLE states (32\*5=160 files). Having the full set of large signal data results in a more accurate model. Support using only 3 files was introduced to minimize the data collection process with some loss in accuracy for the other 29 CTLE states. When only 3 CTLE states are used, their derived nonlinear compensation factors are interpolated to generate nonlinear compensation factors for all 32 states.
- LS\_InputFileNameList.txt the text file listing all large signal files.
  - o In our case, this file has 15 lines.
  - If the large signal data was provided for all 32 states, then this file would have 160 lines.
- If only 3 CTLE states were used for the large signal data, then one additional file is required: LS\_With\_SS\_DataList.csv. This has three lines that map the large signal data to the small signal states using index CTLE-1. Thus, in our example case, using CTLE\_1, CTLE\_8 and CTLE\_31, this file has three lines: 0, 7 and 31.

The example setup for the RxCTLE\_SS Modeling Tool is defined in the file RxCTLE\_SS\_Modeling.txt in the RxCTLE\_SS\_Modeling\_Tool\DataFiles directory. This file has these two lines:

- SetupAnalysis BitRate 25.78125e9 SamplesPerBit 32 NumStepResp 32
- RxCTLE\_Modeling DirName C:\AMI\AFE\RxWaveformSmallSignalData IBIS\_SParamFile RxSerDes\_CTLE.s2p SkipLines 4 ExtractionTStart 5.0500e-08 ExtractionTLength 1.4000e-09

Note: The value provided in these two lines that are specific for this set of example circuit data files.

The example setup for the RxAFE\_NL Modeling Tool is defined in the file RxAFE\_NL\_Modeling.txt in the RxAFE\_NL\_Modeling\_Tool\DataFiles directory. This file has these three lines:

- SetupAnalysis BitRate 25.78125e9 SamplesPerBit 32 Num\_SS\_StepResp 32
   Num LS Resp 3 Num LS Levels 5
- RxAFE\_LS\_Modeling SS\_DirName C:\AMI\AFE\RxWaveformSmallSignalData LS\_DirName C:\AMI\AFE\RxWaveformLargeSignalData IBIS\_SParamFile RxSerDes\_CTLE.s2p SkipLines 4 ExtractionTStart 5.0500e-08 ExtractionTLength 1.4000e-09

RxAFE\_NonlinearityModeling NumResamplesPerVolt 100 NonlinearityDirName
 C:\AMI\AFE\RxNonlinearityData SkipLines 4

Note: The value provided in these three lines that are specific for this set of example circuit data files.

The RxCTLE Modeling Tool is run by double clicking on the batch file RxCTLE\_Modeling.bat in the RxCTLE\_SS\_Modeling\_Tool directory and results in these two generated files in the RxWaveformSmallSignalData directory that will be used in the IBIS-AMI models.

- RxSerDes\_CTLE.s2p.s4p
  - Note: This name is specific to the example file. It will be based on the name the user sets for the S2P file.
- Combined.OutputStepExtracted.InputWithIBISDeembedded.csv

The RxAFE\_NL Modeling Tool is run by double clicking on the batch file RxAFE\_NL\_Modeling.bat in the RxAFE\_NL\_Modeling\_Tool directory and results in these six generated files in the RxWaveformLargeSignalData directory that will be used in the IBIS-AMI models.

- RxSerDes\_CTLE.s2p.s4p
  - Note: This name should be the same as was used for the RxCTLE\_SS tool.
- Combined.csv
- Combined.InputStepExtracted.WithIBIS Impulse.InputStepLevels.csv
- Combined.OutputStepExtracted.InputWithIBISDeembedded.GainFactors.csv
- Combined.OutputStepExtracted.InputWithIBISDeembedded.GainSplitFactors.csv
- Combined.OutputStepExtracted.InputWithIBISDeembedded.RootValues.csv
- Nonlinearity Combined.csv

When the Visual Studio solution is generated and the Visual Studio build process is run, these files are generated in the RxAMI\_Solution\_SerDes\_AFE\_NL\output-vs2019\Release-AMI directory.

- RxSerDes\_AFE\_NL.ami
- RxSerDes\_AFE\_NL\_Test.ami
- RxSerDes\_AFE\_NL\_TestWaveform.ami
- RxSerDes AFE NL TestWaveformOut.ami
- RxSerDes\_AFE\_NL\_TestWaveform\_x64.dll
- RxSerDes AFE NL x64.dll
- RxSerDes\_AFE\_NL.ibs
- RxSerDes\_AFE\_NL\_Test.ibs
- RxSerDes\_AFE\_NL\_TestWaveform.ibs
- RxSerDes\_AFE\_NL\_TestWaveformOut.ibs

These files along with the RxSerDes\_CTLE.s2p.s4p files can be used in any Channel Simulator on the Windows PC that supports the IBIS 7.0 specification or later.

Use of these IBIS-AMI files a channel simulator is discussed in the separate document RxSerDes\_AFE\_NL\_Use\_In\_SerDesDesign\_on\_Windows.pdf.

# **Topics for Further Consideration**

Additional SerDesDesin.com Premium Tools are available with local download and installation on a user's Windows 64-bit PC.

For details see the links:

https://www.serdesdesign.com/home/web\_documents/SerDes\_Design\_Premium\_Tool.pdf https://www.serdesdesign.com/home/store/

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- <u>Description</u>; <u>Guarantee</u>; <u>Webinar</u>; Premium Tool SerDes\_System\_Tool
- Guarantee; Premium Tool SerDes\_IBIS-AMI\_Model\_Generation\_Tool
- This an add-on to the SerDes System Tool and includes the SerDesDesign.com IBIS-AMI Model Development Environment for Windows/Linux.
- Guarantee; Premium Tool SerDes IBIS-AMI TxFFE Modeling Tool

 <u>Description</u>; <u>Guarantee</u>; <u>Webinar</u>; Premium Tool – SerDes IBIS-AMI Rx/Tx CTLE Modeling Tool

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